	Enrollr	nent No: _	Exam Seat No:										
			C.U.	SHAF	IUN	NIVI	ERSIT	Y					
	Summer Examination-2018												
	Subject	ct Name: Analog Electronics Circuits											
	Subject	t Code: 4	Code: 4TE03AEC1			Branch: B.Tech (Electrical)							
	Semest	er: 3	Date: 2	22/03/2018		Time:	02:30 To 05:3	30	Marks: 70				
	Instruct (1) (2) (3) (4)	ions: Use of Pro Instruction Draw near Assume s	ogrammable ns written o t diagrams a uitable data	e calculator & n main answe and figures (if if needed.	z any oth er book f necessa	ner electr are strict ary) at rig	onic instrume ly to be obeye ght places.	nt is pro d.	ohibited.				
Q-1		Attempt	the followi	ng questions	:					(14)			
	1)	Average of	output volta	ge of full way	ve rectif	ier is	than half	fwave	rectifier.				
		A) Le	ess than	B) Greater	than	C) Equ	al to D) N	one of	the above				
	2)	Which one of the below IC is used to obtain +9 V constant voltage?											
		A) IC	2 7805	B) IC 7812	C) IC	7912	D) IC 7809)					
	3)	In a recti filter.	fier circuit,	, ripple facto	or	_with inc	crease in the	value o	of capacitor				
		A) Remai	ns Constant	t B) Decr	eases	C) Inc	reases D) l	None of	f the above				
	4)	In a trans terminal o	istor series of the transis	regulator circ	cuit, zen	er diode	is connected	with					
		A) Collec	tor B) Emitter	C) B	ase	D) None of	of the a	bove				
	5)	For a common emitter amplifier, h-parameter model, h_{ie} stands for											
		A)	Output in	npedance	B)	Output	Conductance						
		C)	Input Con	nductance	D)	Input I	mpedance						
	6)	Which on gain great	e of the be er than unit	low amplifier	r config	uration c	ontains voltag	ge gain	and current				



- A) Common Emitter B) Common Base
- C) Common Collector D) None of the above
- 7) If an amplifier is cascaded in two stages, gain of the stage 1 is Av₁ and gain of stage 2 is Av₂, then total gain of the amplifier is ______

A) Av_1 / Av_2 B) $Av_1 + Av_2$ C) $Av_1 x Av_2$ D) $Av_1 - Av_2$

8) In an amplifier circuit, Q point of transistor is biased in which region?

A) Cut-off B) Active C) Saturation D) None of the above

9) The maximum efficiency of transformer coupled **class** *A* amplifier is_____

A) 25 % B) 40 % C) 80 % D) 50%

10) If a negative feedback is applied to the amplifier, the gain of amplifier due to feedback _____

A) Remains constant B) Increases C) Decreases D) None of the above

11) For a sinusoidal oscillator circuit, what is the Barkhausen criteria for oscillator circuit?

A) $\beta A < 1$ B) $\beta A = 1$ C) $\beta A = -1$ D) $\beta A = 0$

12) An input voltage $v_{in} = 50mV$ is applied at non-inverting terminal of the op-amp having open loop gain 200,000. What will be the output voltage?

- A) 10,000 V B) -10,000 V C) 8000 V D) 8000 V
- **13**) Which one of the below is a non-sinusoidal oscillator?
 - A) R-C phase shift oscillator B) Hartley oscillator
 - C) Multivibrator D) Wein-Bridge oscillator
- 14) For an ideal operational amplifier which one is the false statement?
 - A) Infinite input resistance B) Zero output resistance
 - C) Infinite Voltage Gain D) Zero input resistance

Attempt any four questions from Q-2 to Q-8

Q-2		Attempt all questions				
	(a)	Draw the circuit diagram and waveforms of full wave bridge rectifier. Explain its	07			
		operation.				



(b) Explain the operation of zener shunt regulator with varying input voltage. [Line 07 regulation]

Q-3 Attempt all questions

(a) For a zener regulator shown in the figure, calculate the range of input voltage for 07 which output will remain constant.

$$V_z = 6.1 V, I_{zmin} = 2.5 mA, I_{zmax} = 25 mA, r_z = 0 \Omega.$$



(b) Explain the fixed bias circuit for BJT.

Q-4 Attempt all questions

- (a) Draw the h-parameters model for common emitter transistor and obtain hparameters for the BJT.
- (b) Determine the following for the fixed bias configuration. 07





(14)

07

(14)

Q-5 Attempt all questions

(14)

(14)

- (a) For series fed class A amplifier (Direct coupled class A amplifier) show that 07 maximum efficiency is 25 %.
- (b) Draw the circuit of class B push-pull amplifier and explain its operation. Derive the 07 equation of conversion efficiency class B push pull amplifier.

Q-6 Attempt all questions (14)

- (a) Explain reasons for applying negative feedback to the amplifier. 07
- (b) Explain the operation of RC phase shift oscillator and derive the condition 07 $f = \frac{1}{2\pi\sqrt{3}RC}$ for sustained oscillations.

Q-7 Attempt all questions

- (a) Draw the circuit diagram of Wein Bridge oscillator circuit and obtain the condition 07 $f = \frac{1}{2\pi RC}$ for sustained oscillation.
- (b) Explain operating principle of sinusoidal oscillator and give Barkhausen criteria for 07 oscillation.

Q-8 Attempt all questions (14)

- (a) List the characteristics of an ideal op-amp.
 (b) Explain the following amplifiers for open-loop configurations.
 07
 - i) Inverting amplifier ii) Non-inverting amplifier

